

2154
JPD

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of Clive M. Philbrick, et al.

Ser. No.: 109/085,802

Filing Date: February 26, 2002

Examiner: Larry Donaghue

Atty. Docket No.: ALA-019

GAU: 2154

For: TCP/IP OFFLOAD DEVICE WITH FAST-PATH TCP ACK GENERATING
AND TRANSMITTING MECHANISM

July 21, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

8th Supplemental Information Disclosure Statement

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, applicants bring twenty-seven Patent reference documents and twelve Non-Patent documents listed on an enclosed two-page form PTO-1449 to the attention of the Examiner in the above-identified application. Copies of the Patent reference documents listed on the enclosed two-page form PTO-1449 are not enclosed.

Citation of these documents shall not be construed as an admission that the documents are prior art with respect to the instant invention, a representation that a search has been made, or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

Respectfully submitted,

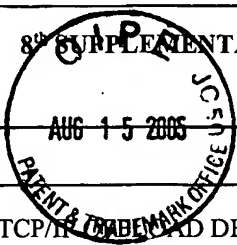
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Date: 7-21-05

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U.S. Department of Commerce, Patent and Trademark Office		Application No.: <u>09/085,802</u>
		Filing date: February 26, 2002
		Inventors: Clive M. Philbrick, et al.
		Group Art Unit: 2154
TCP/IP FORWARD DEVICE WITH FAST-PATH TCP ACK GENERATING AND TRANSMITTING MECHANISM		Examiner name: Larry Donaghue
		Attorney Docket No. ALA-019

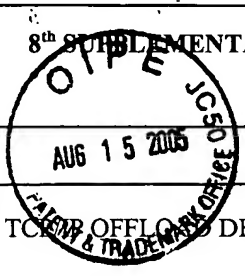
U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date; If Appropriate
	A	2003/0165160	9/4/03	Minami et al.	370	466	
	B	2003/0066011	4/11/02	Oren	714	758	
	C	2001/0053148	12/20/01	Bilic et al.	370	389	
	D	2001/0048681	12/6/01	Bilic et al.	370	389	
	E	2001/0014954	8/16/01	Purcell et al.	714	4	
	F	2001/0014892	8/16/01	Gaither et al.	707	200	
	G	2001/0013059	8/9/01	Dawson et al.	709	217	
	H	4,589,063	5/13/86	Shah et al.	710	8	
	I	5,598,410	1/28/1997	Stone	370	469	
	J	5,778,013	7/7/98	Jedwab	714	807	
	K	5,970,804	8/4/98	Osborne	395	200.75	
	L	6,038,562	3/14/00	Anjur et al.	707	10	
	M	6,070,200	5/30/00	Gates et al.	710	20	
	N	6,101,555	8/8/00	Goshey et al.	709	321	
	O	6,145,017	11/7/00	Ghaffari	710	5	
	P	6,157,955	12/5/00	Narad et al.	709	228	
	Q	6,172,980	1/9/01	Flanders et al.	370	401	
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	T	6,298,403	10/2/01	Suri et al.	710	100	
	U	6,334,153	12/25/01	Boucher et al.	709	230	
	V	6,389,479	5/14/02	Boucher	709	243	
	W	6,421,742	7/16/02	Tillier	710	1	
	X	6,480,489	11/12/02	Muller et al.	370	389	
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	AA	6,157,955	12/5/00	Narad et al.	709	228	

Examiner

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

U.S. Department of Commerce, Patent and Trademark Office		Application No.: 60 085,802
8th SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT 		Filing date: February 26, 2002
		Inventors: Clive M. Philbrick, et al.
		Group Art Unit: 2154
TCP OFFLOADING DEVICE WITH FAST-PATH TCP ACK GENERATING AND TRANSMITTING MECHANISM		Examiner name: Larry Donaghue
		Attorney Docket No. ALA-019

OTHER ART—NON PATENT LITERATURE DOCUMENTS

*Examiner Initial	Cite No.	(Including Author, Title, Date, Pertinent Pages, Etc.)
	1	WindRiver article entitled "Tornado: For Intelligent Network Acceleration", copyright Wind River Systems, 2001, 2 pages.
	2	WindRiver White Paper entitled "Complete TCP/IP Offload for High-Speed Ethernet Networks", Copyright Wind River Systems, 2002, 7 pages.
	3	Intel article entitled "Solving Server Bottlenecks with Intel Server Adapters", Copyright Intel Corporation, 1999, 8 pages.
	4	Schwaderer et al., IEEE Computer Society Press publication entitled, "XTP in VLSI Protocol Decomposition for ASIC Implementation", from 15 th Conference on Local Computer Networks, 5 pages, Sept. 30 – Oct. 3, 1990.
	5	Beach, Bob, IEEE Computer Society Press publication entitled, "UltraNet: An Architecture for Gigabit Networking", from 15 th Conference on Local Computer Networks, 18 pages, Sept. 30 – Oct. 3, 1990.
	6	Chesson et al., IEEE Symposium Record entitled, "The Protocol Engine Chipset", from Hot Chips III, 16 pages, Aug. 26-27, 1991.
	7	Maclean et al., IEEE Global Telecommunications Conference, Globecom '91, presentation entitled, "An Outboard Processor for High Performance Implementation of Transport Layer Protocols", 7 pages, Dec. 2-5, 1991.
	8	Ross et al., IEEE article entitled "FX1000: A high performance single chip Gigabit Ethernet NIC", from Compcon '97 Proceedings, 7 pages, Feb. 23-26, 1997.
	9	Strayer et al., "Ch. 9: The Protocol Engine" from XTP: The Transfer Protocol, 12 pages, July 1992.
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	11	Koufopavlou et al., IEEE Global Telecommunications Conference, Globecom '92, presentation entitled, "Parallel TCP for High Performance Communication Subsystems", 7 pages, Dec. 6-9, 1992.
	12	Lilienkamp et al., Publication entitled "Proposed Host-Front End Protocol", 56 pages, Dec. 1984.
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Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.